

WP-conveners 2nd phone meeting

- Date 7 June 2006
- Time
 - ◆ 07:00 west coast
 - ◆ 10:00 east coast
 - ◆ 16:00 central Europe
 - ◆ 23:00 Japan
- Phone +494089981390 code 52872#

Agenda for 2nd phonemeeting

AGENDA

- 1.General information
- 2.Overall time schedule for the LP work
- 3.Future phonemeetings
- 4.Setup for email lists
- 5.Setup for the LCTPC/LP web page
- 6.Experience with the endplate for the Aleph TPC
and comparison with ideas up to now
- 7.AOB

AGENDA

-1.General information

Latest version of PRC written report at

<http://mppmu.mpg.de/~settles/tpcprc08052006r.pdf>

This gives an overview of design issues and related R&D to be organized for the LP

My comments to the PRC closed session are at

<http://mppmu.mpg.de/~settles/lctpcremarks.ppt>

The PRC recommendations are expected to be released this week and will be circulated, with a proposal for next steps for the LCTPC/LP collaboration

Agenda

-2. Overall time schedule for the LP work

Upcoming milestones for the Eudet facility

PCMAG magnet available -----12/2006
Prototype TPC altro preampboard available-- 12/2006
Prototype TimePix available ----- 12/2006
TPC fieldcage available ----- 6/2007
1000 altro channels available -----12/2007
SiTPC ready (Gem or Micromegas) -----12/2008

Most urgent point for LP is to

- ◆ Design layout of endplate --- finalize 12/2006?

Then

◆ Manufacture/test endplates ----- by 12/2007?
◆ Figure out order of installation, R&D for 2008/9

AGENDA

-3.Future phonemeetings

Since the ideas are very diverse at the moment (see point 6. of the agenda), we need rather frequent 1-hour phonemeetings to work through the options.

Proposal:

Bi-weekly meetings: Tuesdays (or Mon., Wed.?)
20/06, 04/07, 18/07 (Vancouver)

At Vancouver, we can decide frequency of next meetings.

After Vancouver there are (in addition to phonemeetings):

- Paris, WP1 workshop? (between 4 and 15 September)
- Eudet annual meeting MPI-Munich (18-20 October)
- Valencia LC workshop (6-10 November)

AGENDA

-4.Setup for email lists

a) For the latest list of groups: see next 5 slides.

Each WP can generate its mailing list from that list of groups and the Cornell wiki site:

<https://wiki.lepp.cornell.edu/ilc/bin/view/Public/WW5/TrackLCTPCcollab>

(Feedback from the groups not urgent and will take care of itself after a while; however we have to know by autumn)

N.B. This website has recently been redesigned (there are still a couple of problems).

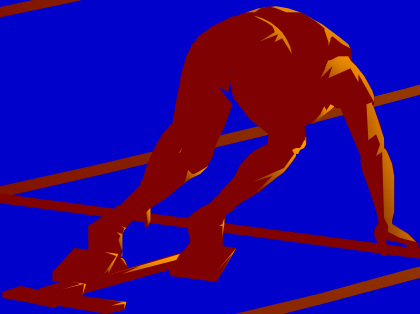
b) For our convener meetings it would be simplest (less administration) to just announce them to the full LCTPC/LP mailing list, so that they are informed of the convener meetings; the whole collaboration should get the minutes also. This should simplify and open the discussion to include everyone. (Ties has generated a new LCTPC/LP maillist based on Cornell wiki.),

What do you think?

Work Packages for the LP/LCTPC

0) Workpackage: TPC R&D programme

A job for the whole LC TPC collaboration to define



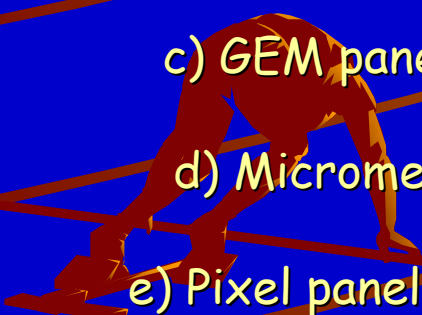
Work Packages for the LP/LCTPC

convener in
white color

1) Workpackage **MECHANICS**

Ron Settles

Groups expressing interest to date

- 
- | | |
|---|--|
| a) LP design (incl. endplate structure) | Cornell, Desy, IPNOrsay, MPI,
+contribution from Eudet |
| Dan Peterson | |
| b) Fieldcage, laser, gas | Aachen, Desy, St.Petersburg,
+contribution from Eudet |
| Ties Behnke | |
| c) GEM panels for endplate | Aachen, Carleton, Cornell, Desy/HH,
Karlsruhe, Kek/CDC, Novosibirsk, Victoria |
| Akira Sugiyama | |
| d) Micromegas panels for endplate | Carleton, Cornell, Kek/CDC,
Saclay/Orsay |
| Paul Colas | |
| e) Pixel panels for endplate | Cern,Freiburg,Nikhef,Saclay,Kek/CDC,
+contribution from Eudet |
| Jan Timmermans | |
| f) Resistive foil for endplate | Carleton, Kek/CDC, Saclay/Orsay |
| Madhu Dixit | |

Work Packages for the LP/LC TPC

2) Workpackage ELECTRONICS

Leif Jonnson

Groups expressing interest to date(others?)

a)"Standard" RO/DAQ for LP:
Leif Jonnson + ?

Aachen, Desy/HH, Cern, Lund,
Rostock, Montreal, Tsinghua,
+contribution from Eudet

b) CMOS RO electronics:
Harry van der Graaf

Freiburg, Cern, Nikhef, Saclay,
+contribution from Eudet

c) Electr., powerswitching, cooling
for LC TPC:

Luciano Musa

Aachen, Desy/HH, Cern, Lund,
Rostock, Montreal, St.Petersburg, Tsinghua,
+contribution from Eudet

Work Packages for the LP/LCTPC

3) Workpackage SOFTWARE

Peter Wienemann

Groups expressing interest to date(others?)

a) LP SW+simul./reconstr.framework:

Peter Wienemann

Bucharest, Desy/HH, Cern, Freiburg,
Carleton, Victoria, +contrib. Eudet

b) TPC simulation, backgrounds

Stefan Roth

Aachen, Carleton, Cornell, Desy/HH,
Kek/CDC, St.Petersburg, Victoria

c) Full detector simulation

Keisuke Fujii

Desy/HH, Kek/CDC, LBNL

Work Packages for the LP/LC TPC

4) Workpackage CALIBRATION

Dean Karlen

Groups expressing interest to date(others?)

a) Fieldmap

Lucie Linssen

Cern,
+contribution from Eudet

b) Alignment

Takeshi Matsuda

Kek/XCDC

c) Distortion correction

Dean Karlen

Victoria

d) Rad.hardness of material

Anatoliy Krivchitch

St.Petersburg

e) Gas/HV/Infrastructure

Desy Postdoc

Desy, Victoria,
+contribution from Eudet

AGENDA

-5.Setup for the LCTPC/LP web page

To start with this will be at

<http://mppmu.mpg.de/~settles/tpc/lp>

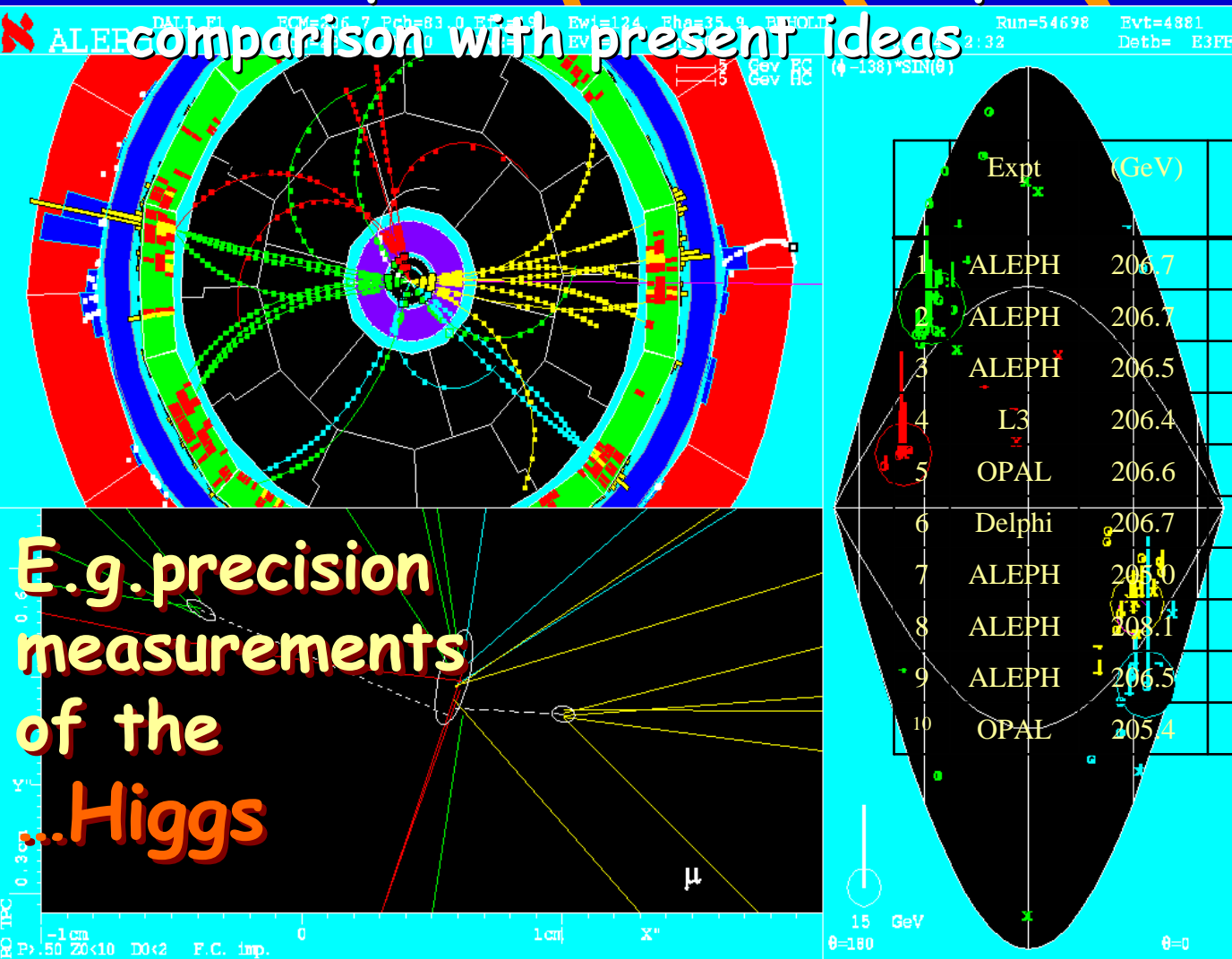
and I hope to get a version up and running within next few days.

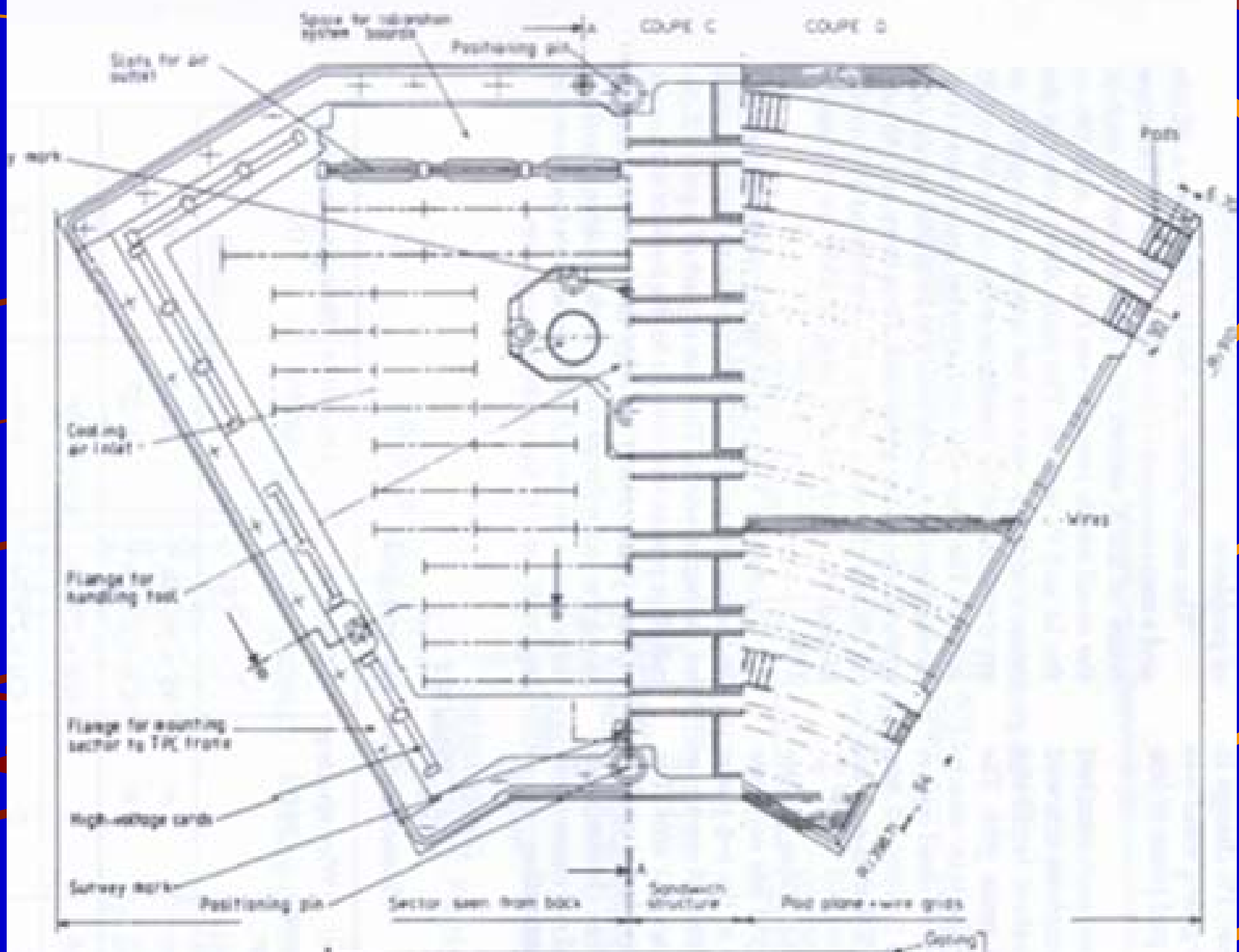
Where should the final website go? Desy?
Cornell? Other?



AGENDA

-6. Experience with the endplate for the Aleph TPC and comparison with present ideas





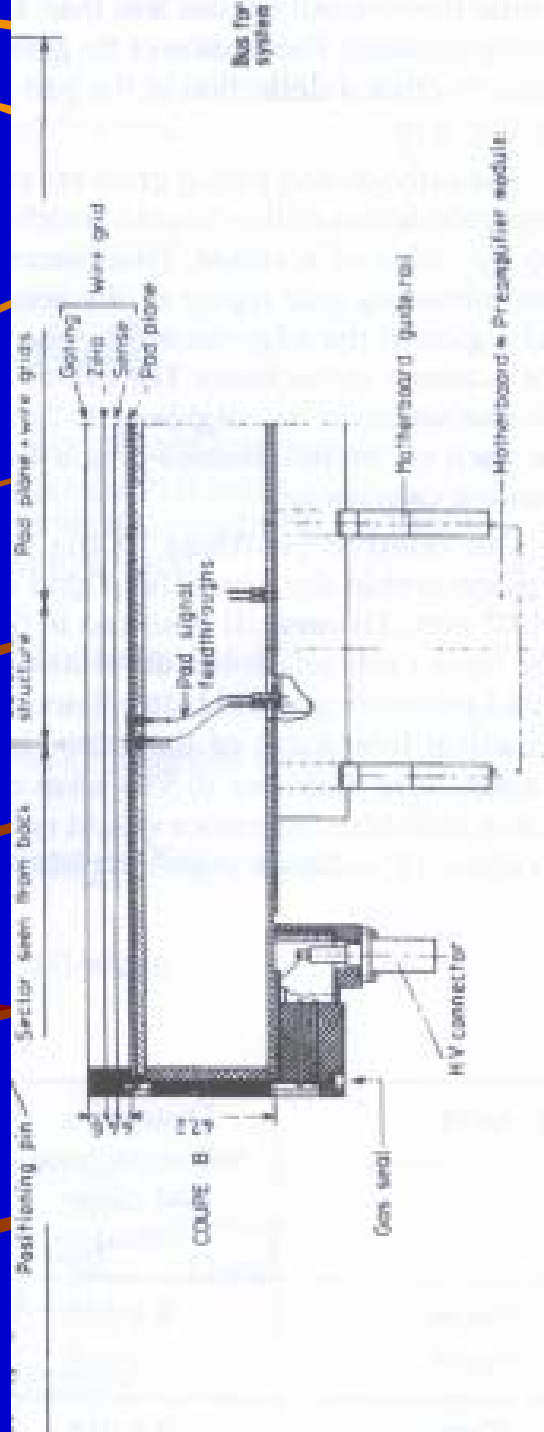
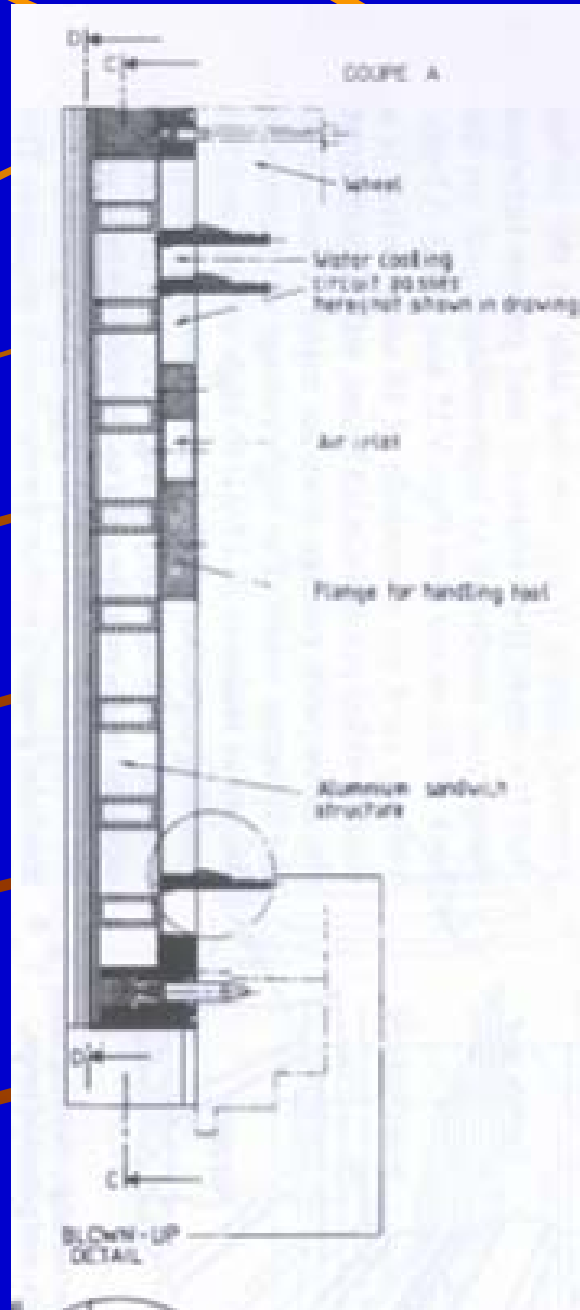
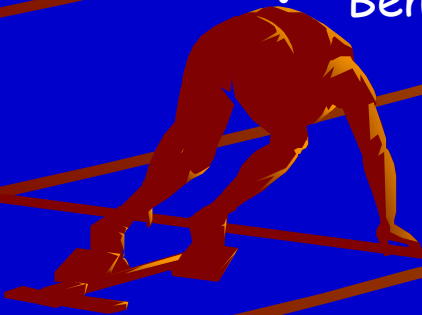


Fig. V15 Mechanical details of the sector design.

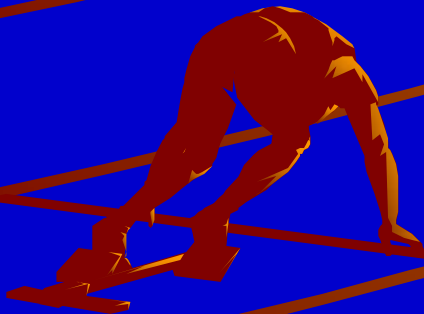


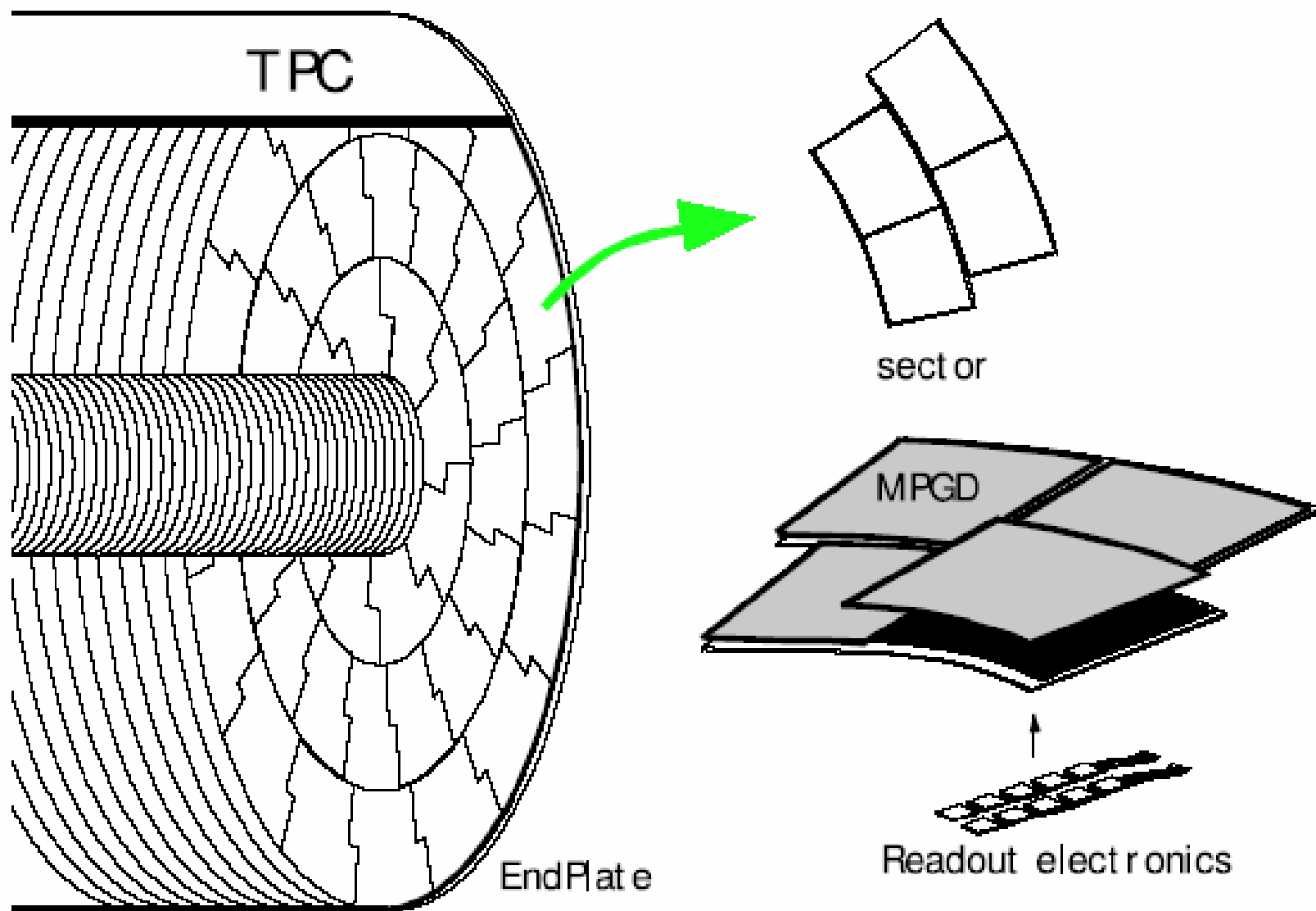
Some features

- Zigzag structure prevented loss of tracks $\theta \sim 22^\circ$
- Alu sandwich structure stiff, lightweight to
 - contain 7mb overpressure
 - provide forced-air thermal insulation between electronics and TPC volume
- Water cooling of 1kW electronics/side in addition
 - 22K channels per side
- Combination water/air cooling blocked all heat to TPC
- Overall thickness $\sim 25\%X_0$ (average) w/o cables
- Bending of endplate
 - 20 micrometers due to 7mb overpressure
 - 5 micrometers due to wire tension



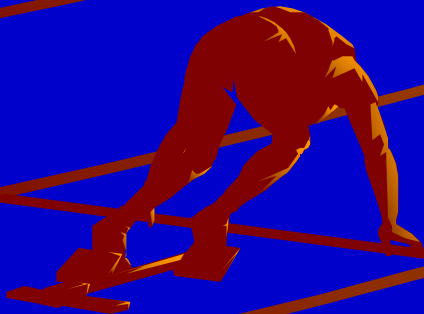
Akira Sugiyama - GLD DOD





RS study - LDC DOD - together with

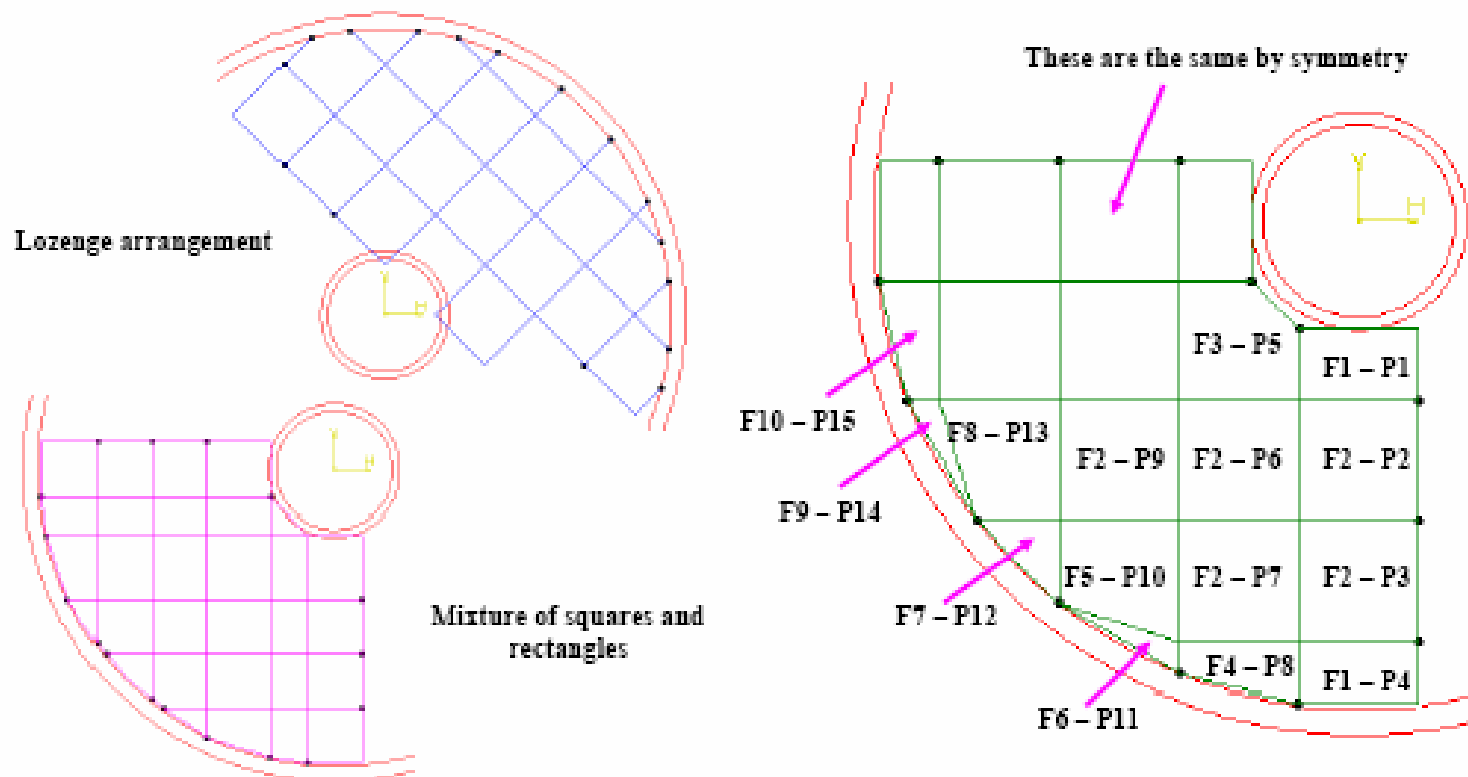
Joel Pouthas
Philippe Rosier
(IPN Orsay)



Arrangements of detectors on the active area of the end cap (1/2)

Squares, rectangles, lozenge of 300/350 mm or 400 mm size

Annotations: *F* is the type number of frames / *P* is for the PADS board



These arrangements need too much different sorts of frames and PADS boards, even if the right drawing is the simplest

Arrangements of detectors on the active area of the end cap (2/2)

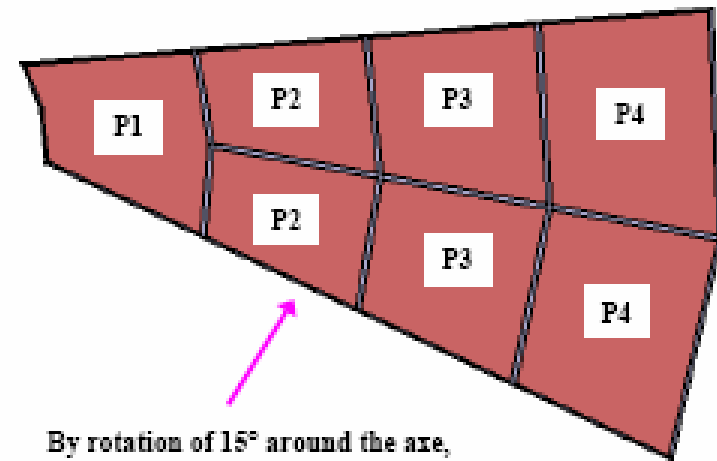
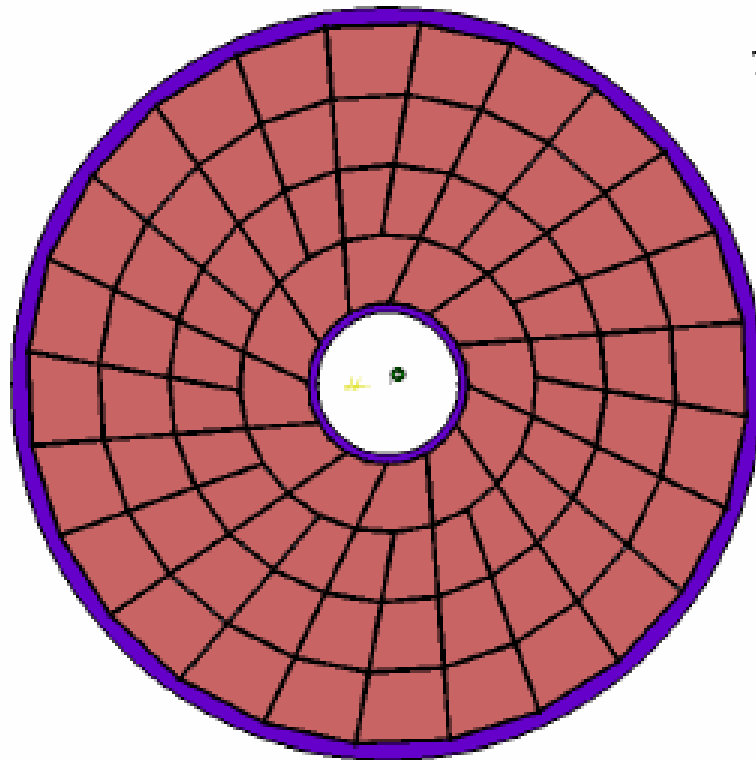
Trapezoidal shapes assembled in iris shape

Annotations: P_x is the type number of PADS boards or frames

12 sectors (30° each) as super modules are defined

On each, 7 modules are fixed

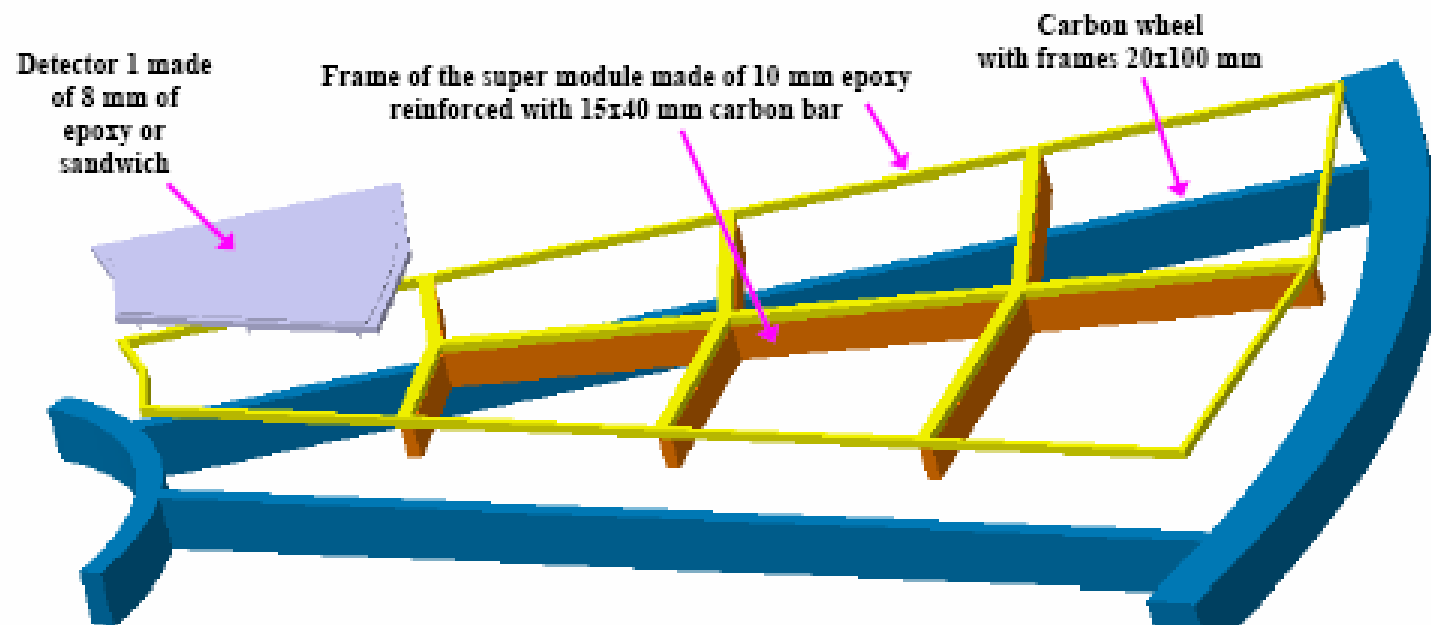
The sizes of detectors are varying from 180 to 420 mm



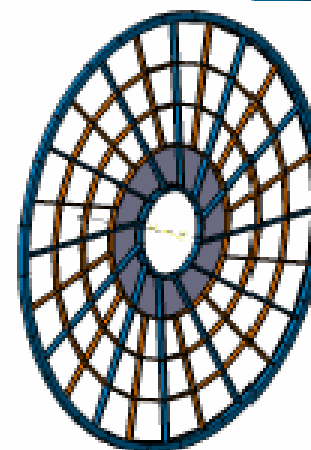
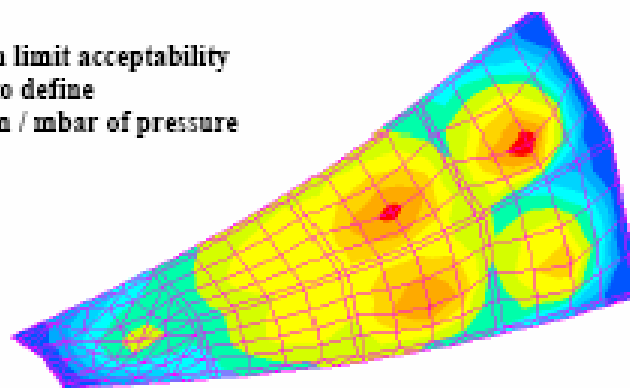
By rotation of 15° around the axis,
these frames are the same

These arrangement seems to be the best as only 4
different PADS are necessary

Principle for a Super Module equipped with detector 1

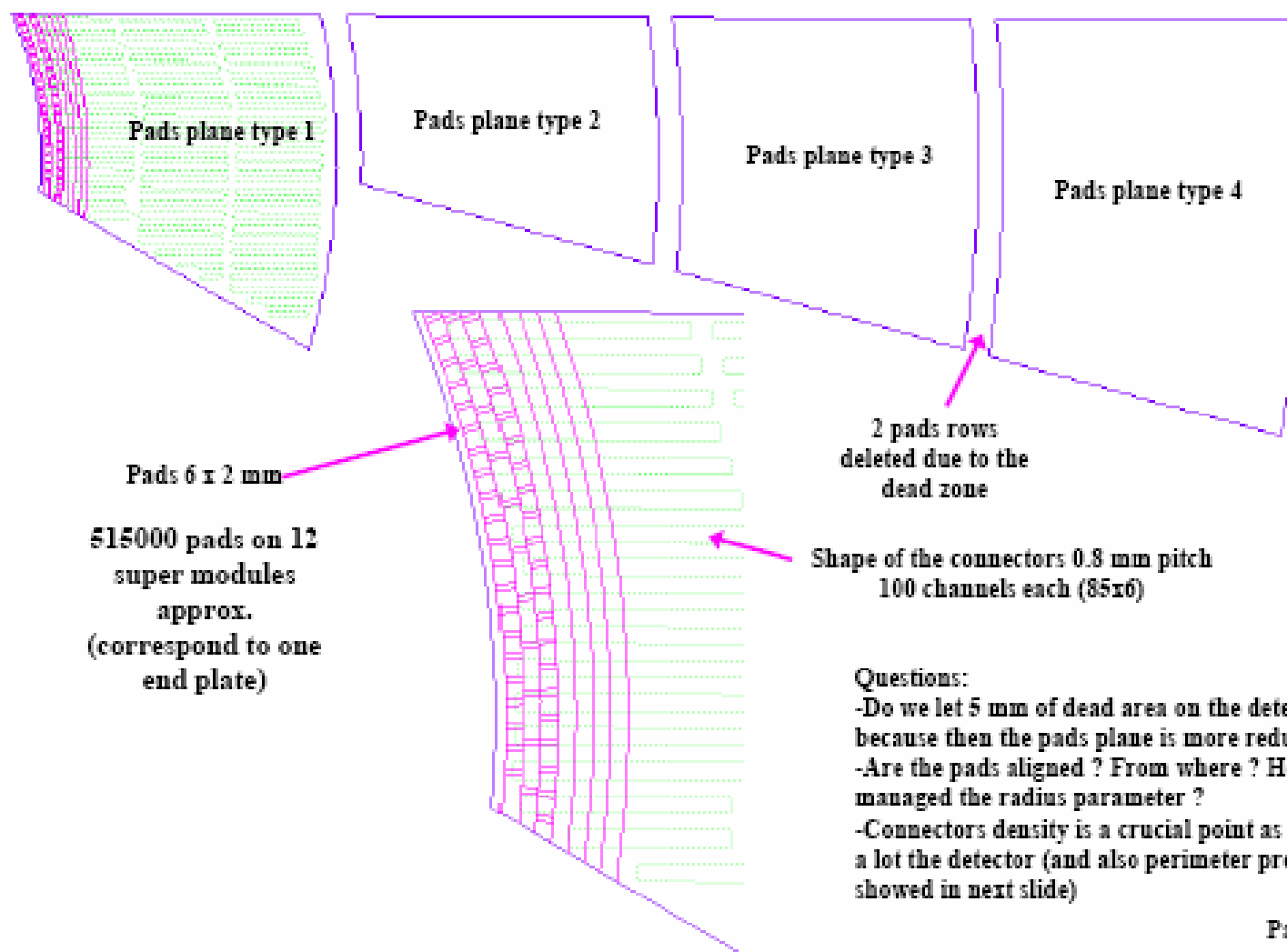


Deformation limit acceptability to define
Here is 20 μm / mbar of pressure



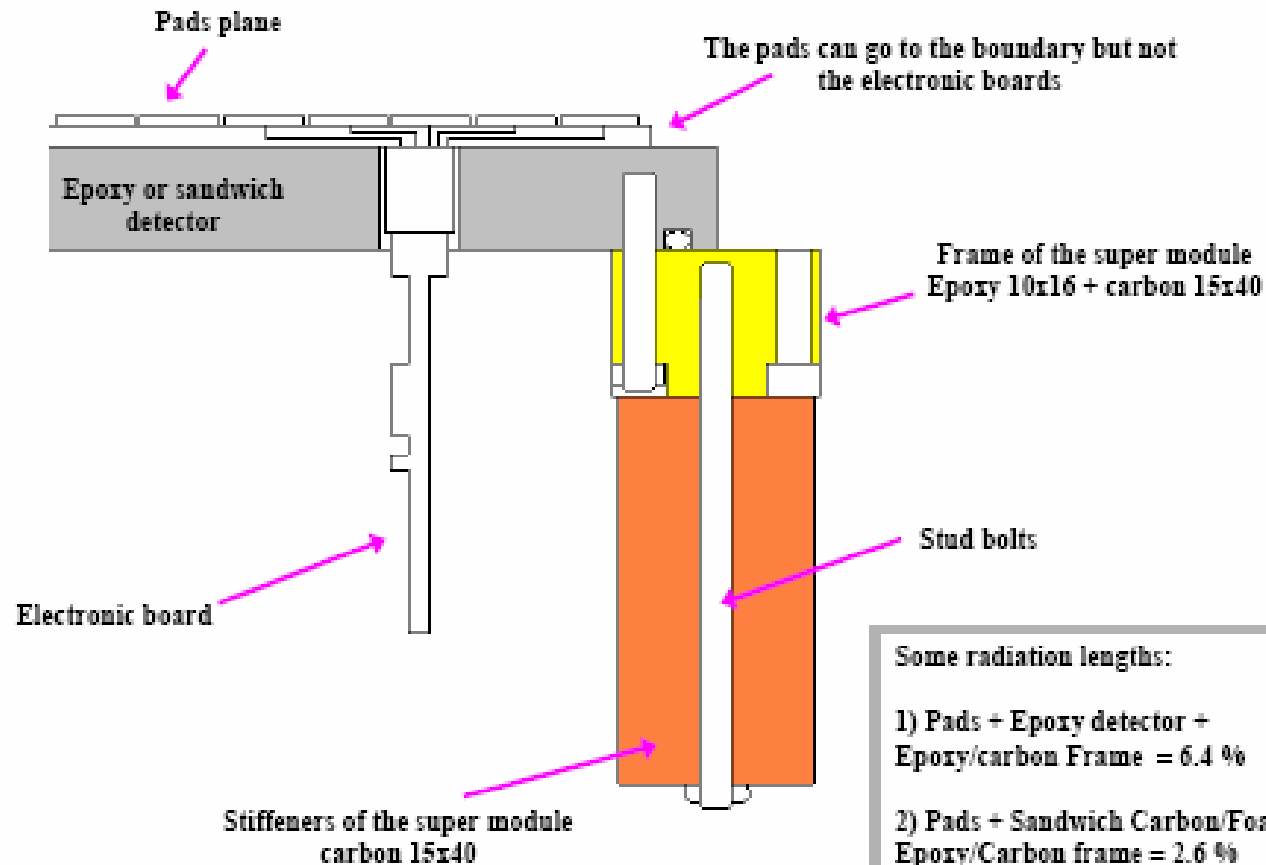
Complete wheel with 12 super modules

Principle for the 4 types of Pads plane



Principle of the boundaries (electronics on the pads plane)

Fixing and sealing of the detectors



Some radiation lengths:

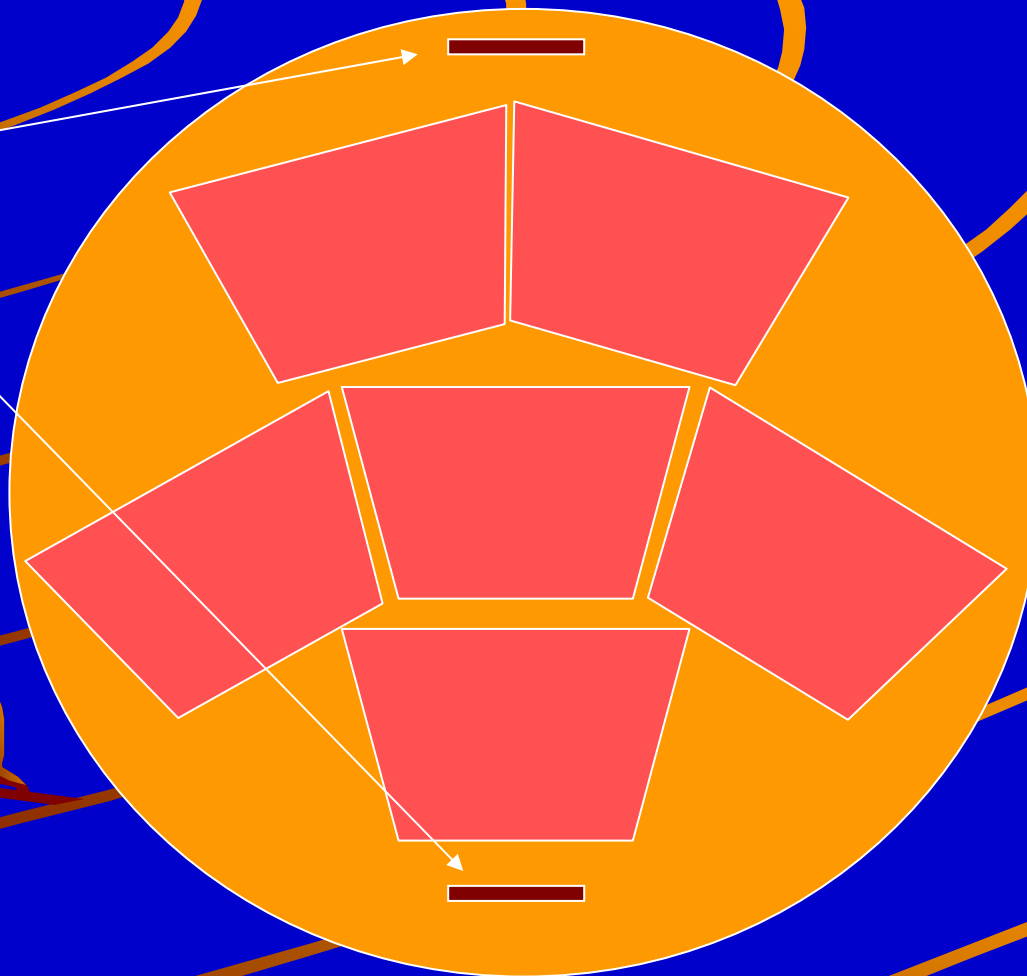
1) Pads + Epoxy detector +
Epoxy/carbon Frame = 6.4 %

2) Pads + Sandwich Carbon/Foam 9 mm +
Epoxy/Carbon frame = 2.6 %

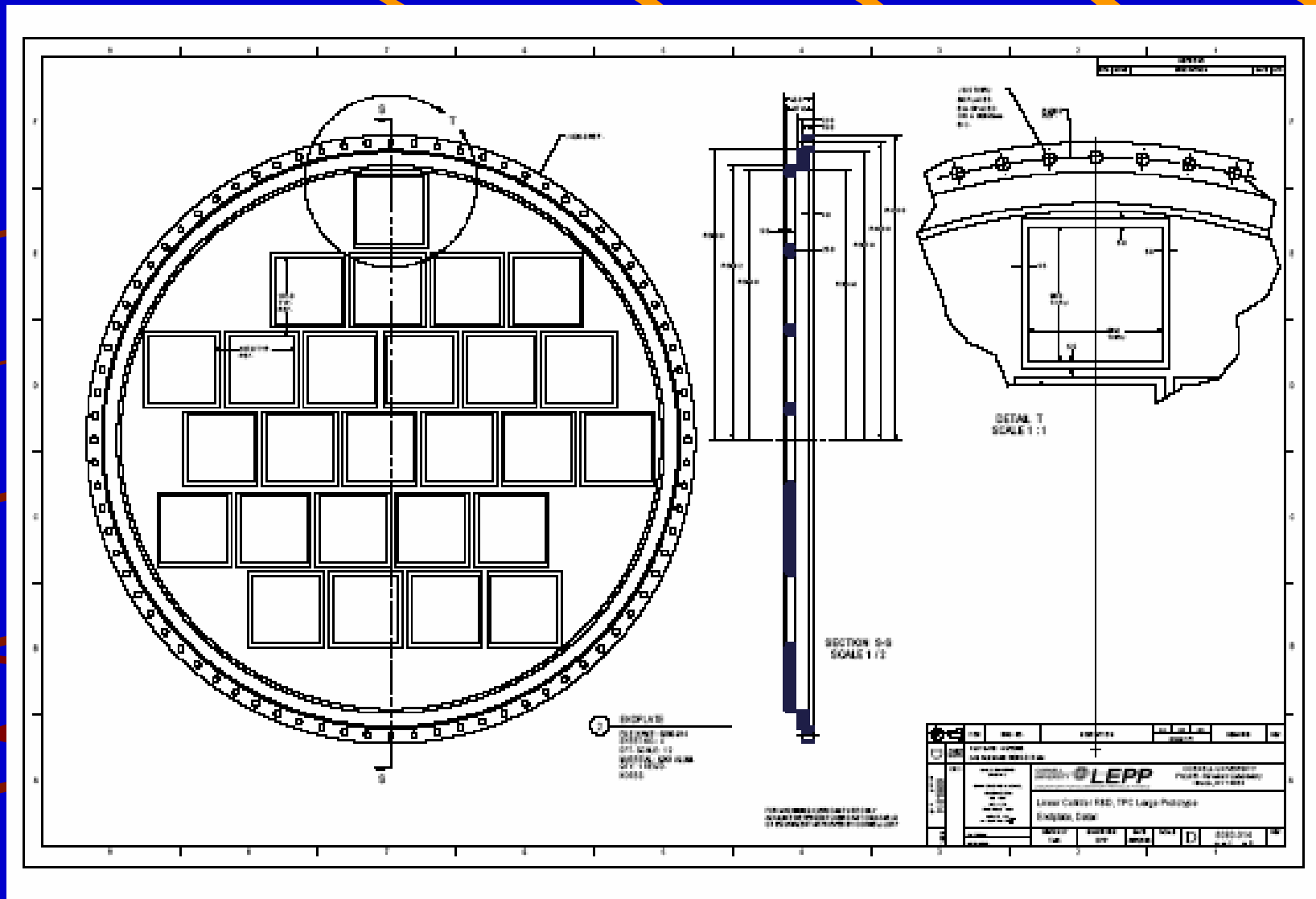
(Without electronics)

Paul Colas - 9 May

Si chip
readout

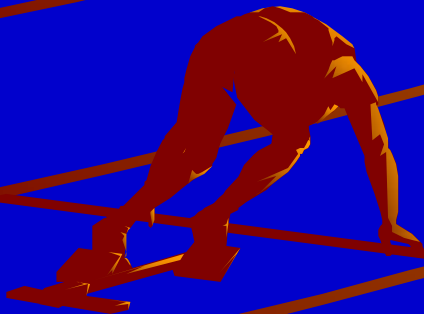


Dan Peterson - 9 May

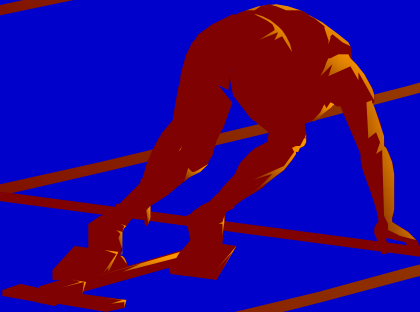


AGENDA

-7.AOB



Back up slides, for reference



TPC R&D Groups (7 June 06)

Europe

RWTH Aachen

Bucharest

CERN

DESY

U Hamburg

U Freiburg

U Karlsruhe

UMM Krakow

Lund

MPI-Munich

NIKHEF

BINP Novosibirsk

LAL Orsay

IPN Orsay

U Rostock

CEA Saclay

PNPI StPetersburg

U Siegen

America

Carleton U

U Cornell

Purdue U

Indiana U

LBNL

MIT

U Montreal

U Victoria

Yale

Asia

Tsinghua U

CDC:

Hiroshima U

Minadamo SU-IIT

Kinki U

U Osaka

Saga U

Tokyo UAT

U Tokyo

Kogakuin U Tokyo

KEK Tsukuba

U Tsukuba

Other

MIT (LCRD)

Temple/Wayne

State (UCLC)

...Other groups interested?

R&D Planning

◆ 1) Demonstration phase

- Continue work with small prototypes on mapping out parameter space, understanding resolution, etc, to prove feasibility of an MPGD TPC. For CMOS/Si-based ideas this will include a basic proof-of-principle.

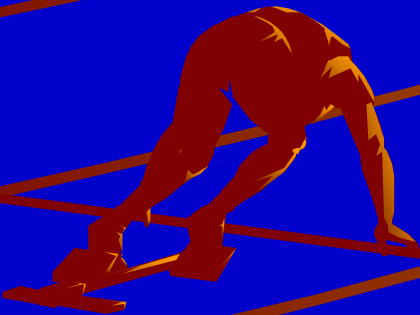
◆ 2) Consolidation phase

- Build and operate the LP, large prototype, ($\varnothing \geq 75\text{cm}$, drift $\geq 100\text{cm}$), with EUDET infrastructure as pedestal, to test manufacturing techniques for MPGD endplates, fieldcage and electronics. Design is starting---building and testing will take another ~ 3 years.

◆ 3) Design phase

- After phase 2, the decision as to which endplate technology to use for the LC TPC would be taken and final design started.

Phase 1 - continue (in
parallel to LP) for
next ~ 2 years



Phase 2

- Basic Idea: LP should be a prototype for the LC TPC design and test as many of the issues as possible as was done before, e.g., TPC90 @ Aleph
- The Eudet infrastructure gives us a starting basis for the LP work
- There other LC TPC R&D issues in addition to building LP which will be planned in conjunction with it

